EE 230 Lecture 15

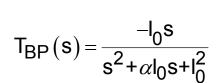
Basic Applications of Operational Amplifiers Differential Amplifiers Impedance Converters Nonideal Op Amp Characteristics (if time permits)

Review from Last Time Applications of integrators to filter design

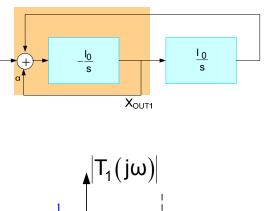
The 2nd order Bandpass Filter

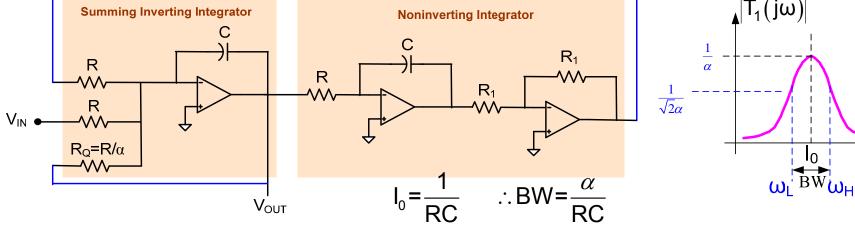
Design Strategy

Assume BW and ω_{P} are specified



XIN





 $\omega_{P} = I_{0}$

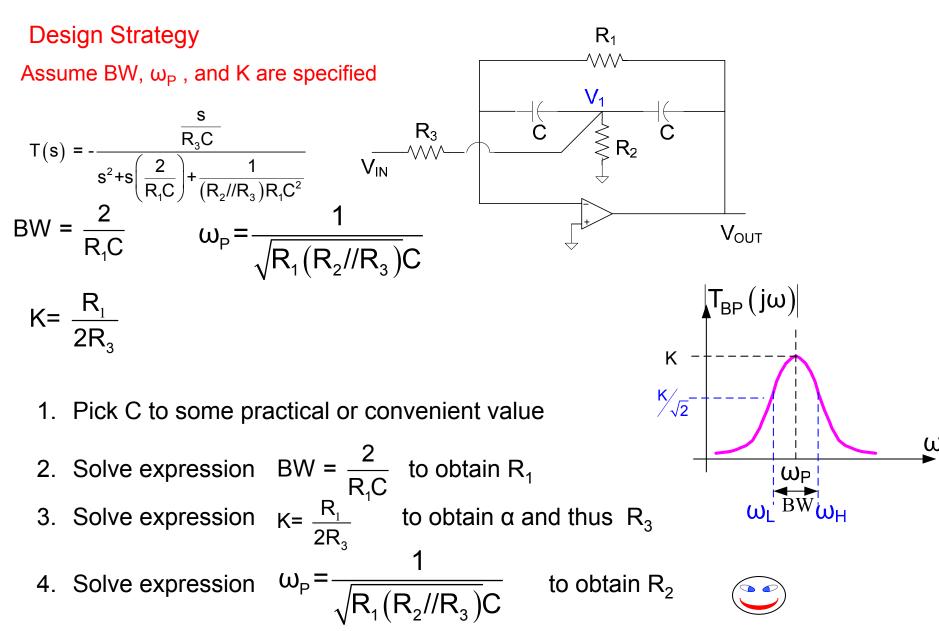
ω

- 1. Pick C (use some practical or convenient value)
- 2. Solve expression $\omega_{P} = \frac{1}{RC}$ to obtain R 3. Solve expression $BW = \frac{\alpha}{RC}$ to obtain α and thus R_{Q}

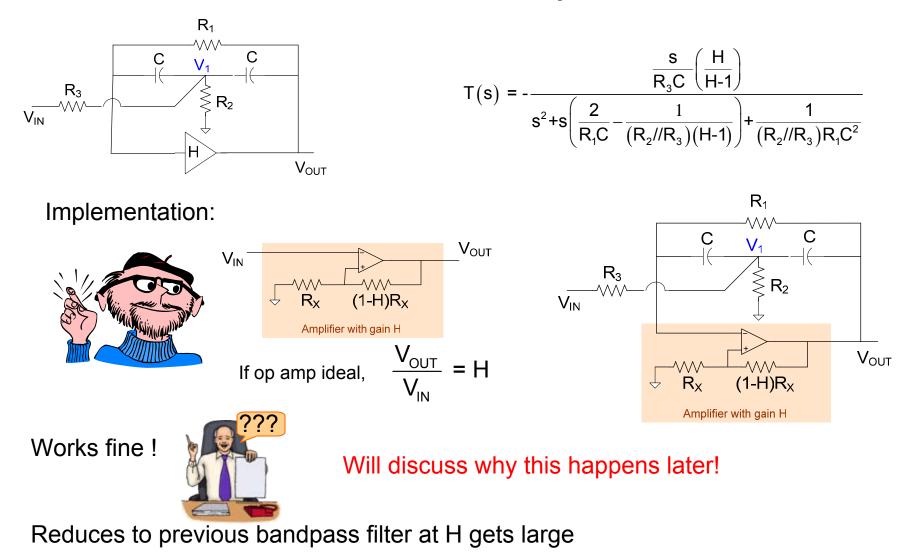


Review from Last Time

Another 2nd-order Bandpass Filter

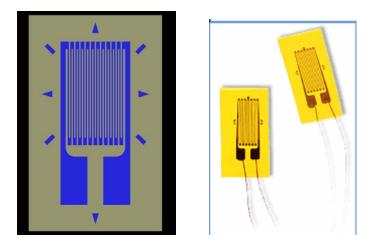


Review from Last Time STAR 2nd-order Bandpass Filter

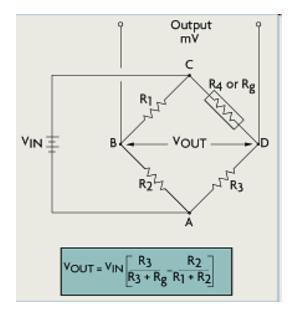


Note that the "H" amplifier has feedback to positive terminal

Many applications exist for difference (differential) amplifiers Differential amplifiers are widely used Strain gage is one application that demonstrates some challenges



Metal foil strain gages

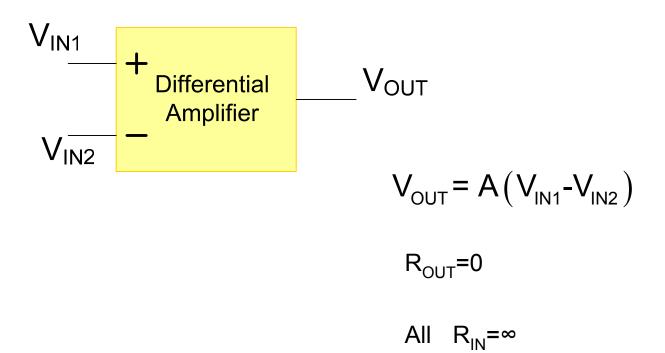


Wheatstone Bridge http://www.omega.com/prodinfo/straingages.html

Assume V_{A} is ground V_{OUT} is very small compared to V_{B} and V_{D}

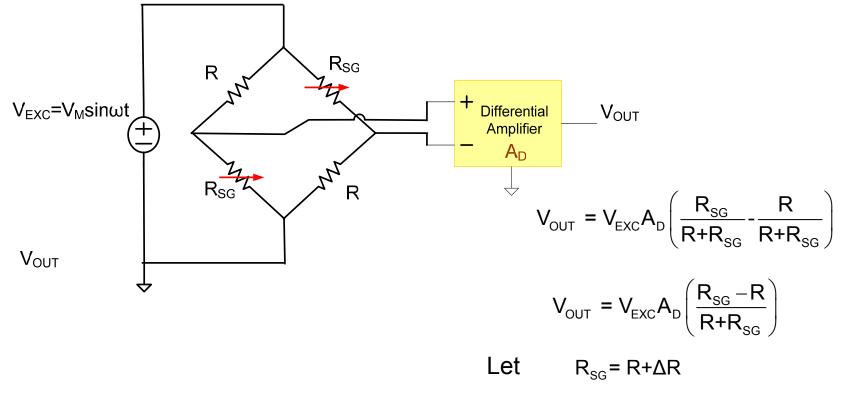
http://en.wikipedia.org/wiki/Strain_gauge

Ideal differential amplifier



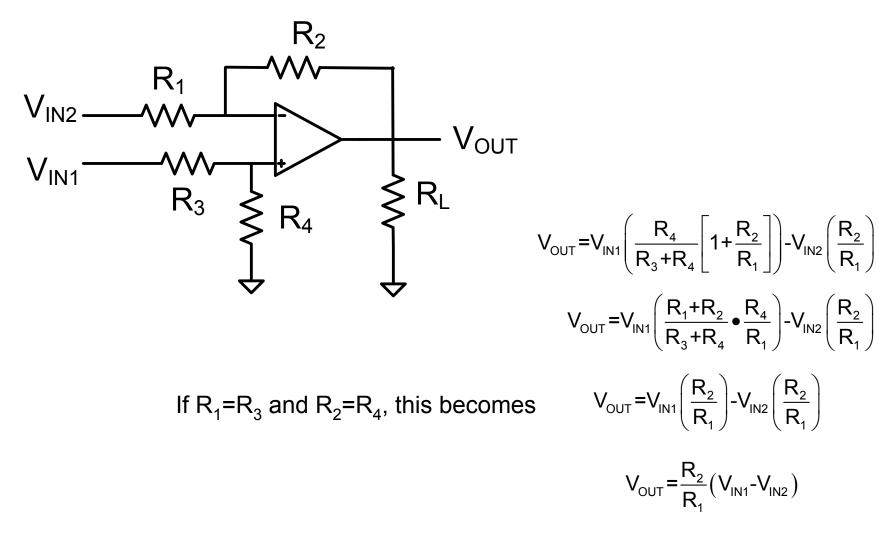
Ideally the output is not dependent upon the size of V_{IN1} or V_{IN2} but only upon their difference This creates a challenge when designing differential amplifiers

Differential amplifier application



$$V_{\text{OUT}} = V_{\text{EXC}} A_{\text{D}} \left(\frac{\Delta R}{2R + \Delta R} \right) \simeq V_{\text{EXC}} A_{\text{D}} \left(\frac{\Delta R}{2R} \right)$$

If ΔR is very small and varies linearly with strain, V_{OUT} varies linearly with strain



Good matching is required to eliminate the dependence on V_{IN1} and V_{IN2}

Common Mode and Difference Mode Gains

$$V_{OUT} = V_{IN1} \left(\frac{R_4}{R_3 + R_4} \left[1 + \frac{R_2}{R_1} \right] \right) - V_{IN2} \left(\frac{R_2}{R_1} \right)$$

Define $V_{INC} = \frac{V_{IN1} + V_{IN2}}{2}$ $V_{IN2} = V_{INC} - \frac{V_{IND}}{2}$

These can be expressed as

$$V_{IND} = V_{IN1} - V_{IN2}$$
 $V_{IN1} = V_{INC} + \frac{V_{IND}}{2}$

$$V_{\text{OUT}} = \left(V_{\text{INC}} + \frac{V_{\text{IND}}}{2}\right) \left(\frac{R_1 + R_2}{R_3 + R_4} \bullet \frac{R_4}{R_1}\right) - \left(V_{\text{INC}} - \frac{V_{\text{IND}}}{2}\right) \left(\frac{R_2}{R_1}\right)$$

$$V_{\text{OUT}} = V_{\text{INC}} \left(\left(\frac{R_1 + R_2}{R_3 + R_4} \bullet \frac{R_4}{R_1} \right) - \left(\frac{R_2}{R_1} \right) \right) + V_{\text{IND}} \left(\frac{1}{2} \right) \left(\frac{R_1 + R_2}{R_3 + R_4} \bullet \frac{R_4}{R_1} + \frac{R_2}{R_1} \right)$$

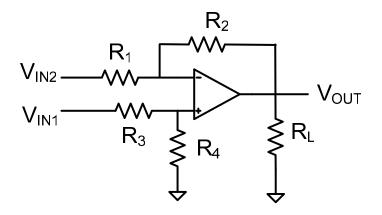
Define

$$A_{COM} = \left(\left(\frac{R_1 + R_2}{R_3 + R_4} \bullet \frac{R_4}{R_1} \right) - \left(\frac{R_2}{R_1} \right) \right) \qquad A_{DIFF} = \frac{1}{2} \left(\frac{R_1 + R_2}{R_3 + R_4} \bullet \frac{R_4}{R_1} + \frac{R_2}{R_1} \right)$$

It follows that

$$V_{OUT} = V_{INC}A_{COM} + V_{IND}A_{D}$$

Good matching is required to eliminate V_{INC} Not easy to adjust or trim the gain

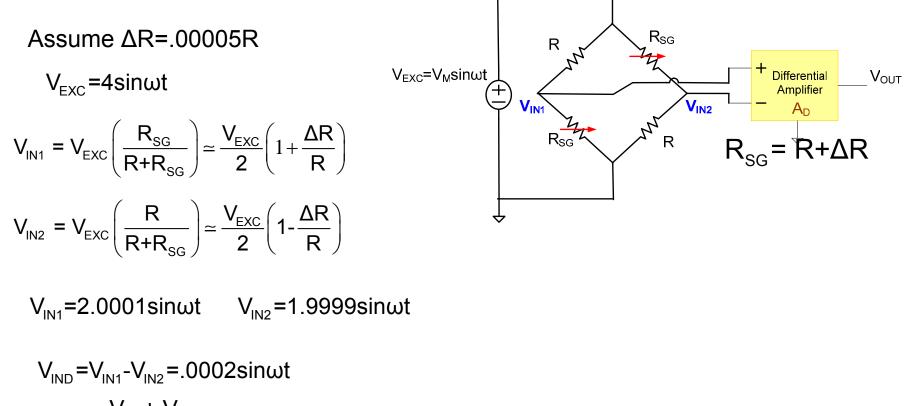


 R_{IN} on each terminal is $\mathsf{R}_3\text{+}\mathsf{R}_4$

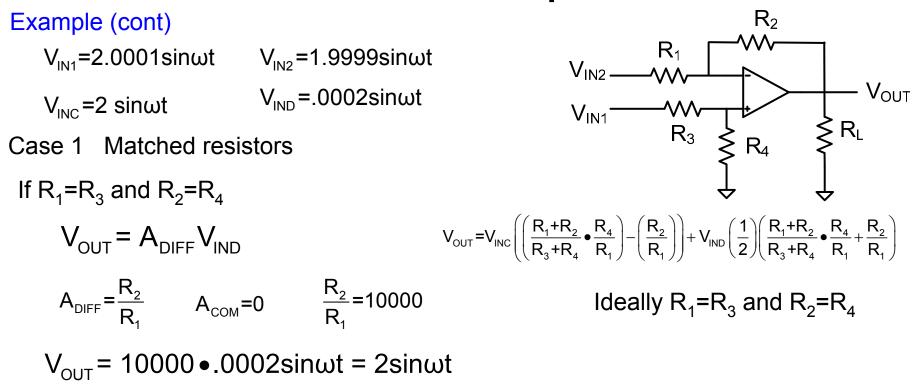
If $R_1 = R_3$ and $R_2 = R_4$

$$A_{COM} = 0 \qquad A_{DIFF} = \frac{R_2}{R_1}$$
$$V_{OUT} = A_{DIFF} V_{IND}$$

Example: Consider the performance of the bridge structure driving the differential amplifier where A_D is nominally 10,000. Neglect loading of bridge with the differential amplifier

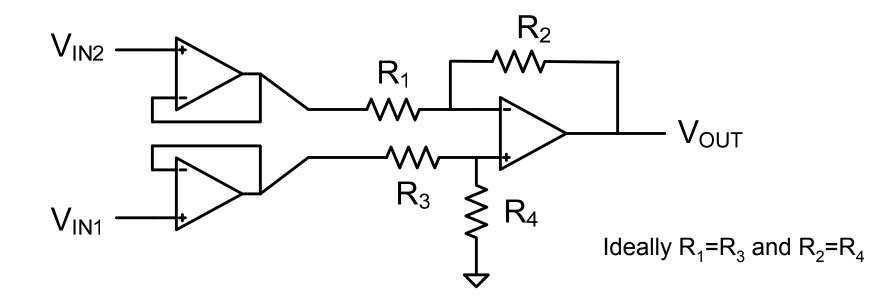


 $V_{INC} = \frac{V_{IN1} + V_{IN2}}{2} \approx 2 \sin \omega t$ All signal information is carried in the difference signal V_{IND} But observe $V_{IND} < < V_{INC}$



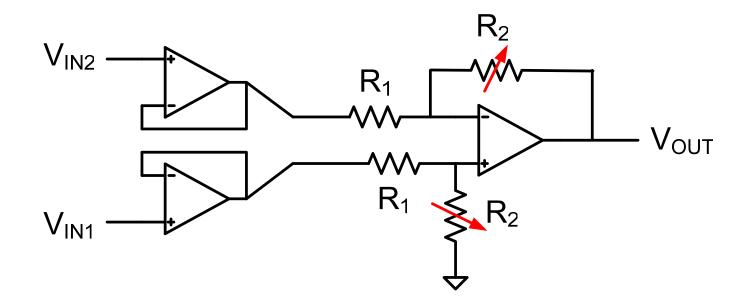
 R_2 Example (cont) V_{IN1}=2.0001sinωt V_{IN2}=1.9999sinωt Vout V_{IND} = .0002sin ω t V_{INC} = 2 sin ω t $\begin{array}{c} - & \\ R_3 \\ R_3 \\ R_4 \end{array}$ V_{IN1}-----Case 1 Matched resistors $V_{out} = 2 \sin \omega t$ $V_{\text{OUT}} = V_{\text{INC}} \left(\left(\frac{R_1 + R_2}{R_3 + R_4} \bullet \frac{R_4}{R_1} \right) - \left(\frac{R_2}{R_1} \right) \right) + V_{\text{IND}} \left(\frac{1}{2} \right) \left(\frac{R_1 + R_2}{R_2 + R_4} \bullet \frac{R_4}{R_4} + \frac{R_2}{R_4} \right)$ Case 2 $R_1 = R_3$, $R_4 = 1.01R_2$ $\frac{R_2}{R_1} = 10000$ Ideally $R_1 = R_3$ and $R_2 = R_4$ $V_{OUT} = V_{INC} \left(\left(\frac{R_1 + R_2}{R_2 + R_4} \bullet \frac{R_4}{R_4} \right) - \left(\frac{R_2}{R_4} \right) \right) + V_{IND} \left(\frac{1}{2} \right) \left(\frac{R_1 + R_2}{R_2 + R_4} \bullet \frac{R_4}{R_4} + \frac{R_2}{R_4} \right)$ $V_{OUT} = V_{INC} \left(\left(\frac{10001R_1}{10101R_1} \bullet \frac{1.01 \bullet 10000R_1}{R_1} \right) - (10000) \right) + V_{IND} \left(\frac{1}{2} \right) \left(\left(\frac{10001R_1}{10101R_1} \bullet \frac{1.01 \bullet 10000R_1}{R_1} \right) + 10000 \right)$ $V_{OUT} = V_{INC} (.0099) + V_{IND} (10000.005)$ $V_{OUT} = 2 \sin \omega t (.0099) + (.0002 \sin \omega t) (10000.005)$ $V_{OUT} = \sin \omega t (.0198) + (\sin \omega t) (2.000001)$ $V_{OUT} = (\sin \omega t) (2.019801)$ Note a 1% error in a resistor causes a 2% error in output

Buffered Difference Amplifier



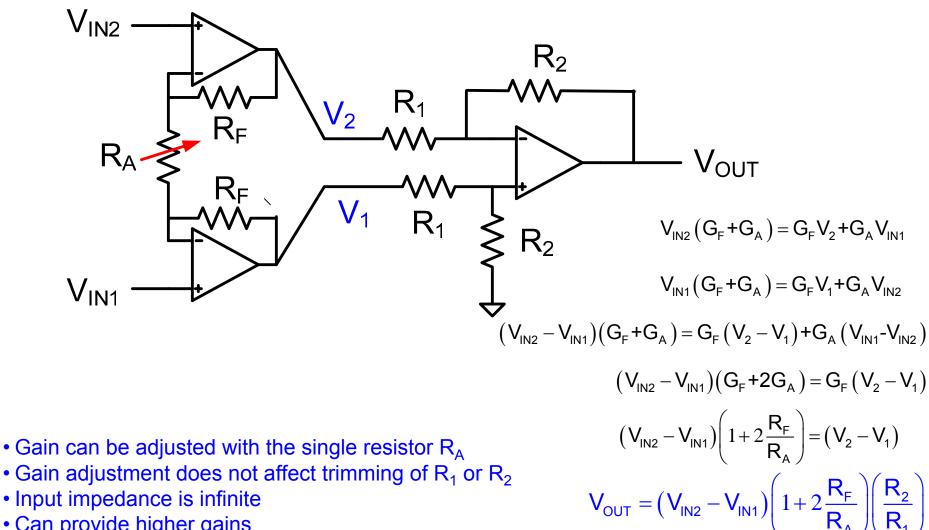
Minimizes loading effect of difference amplifier on source Not easy to adjust or trim the gain

Buffered Difference Amplifier



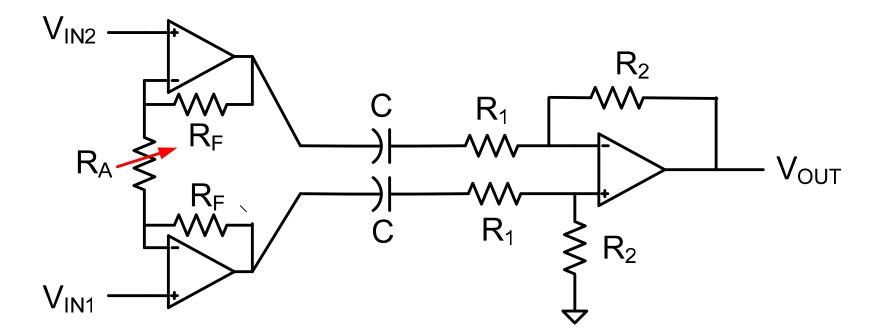
Not easy to adjust or trim the gain

Instrumentation Amplifier



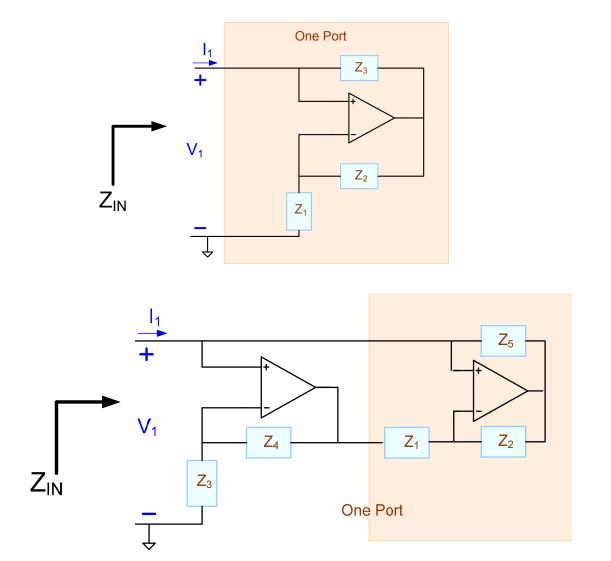
- Input impedance is infinite
- Can provide higher gains

Instrumentation Amplifier

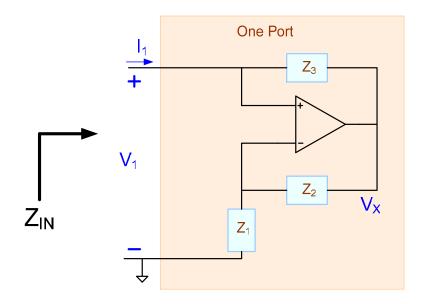


• Can reduce effects of dc offset if gain must be very large

• Must pick C to that frequencies of interest are in passband



Note these circuits are strictly one-ports and have no output node

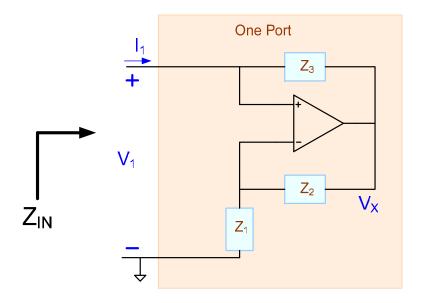


$$V_1(G_1+G_2) = V_XG_2$$

 $I_1 = (V_1-V_X)G_3$

$$Z_{\rm IN} = -\frac{Z_1 Z_3}{Z_2}$$

Observe this input impedance is negative!



This is a negative resistor !

 $Z_{\rm IN} = -\frac{Z_1 Z_3}{Z_2}$

If $Z_2 = 1/sC$, $Z_1 = R_1$ and $Z_3 = R_3$, $Z_{IN} = -sCR_1R_3$

 $Z_{\rm IN} = -\frac{R_1 R_3}{R_2}$

This is a negative inductor !

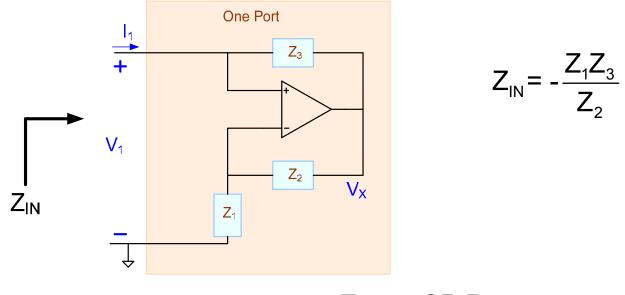
If $Z_2 = R_2$, $Z_1 = 1/sC$ and $Z_3 = R_3$,

If $Z_1 = R_1$, $Z_2 = R_2$ and $Z_3 = R_3$,

 $Z_{IN} = -\frac{R_3}{sCR_2}$

This is a negative capacitor !

This is termed a Negative Impedance Converter

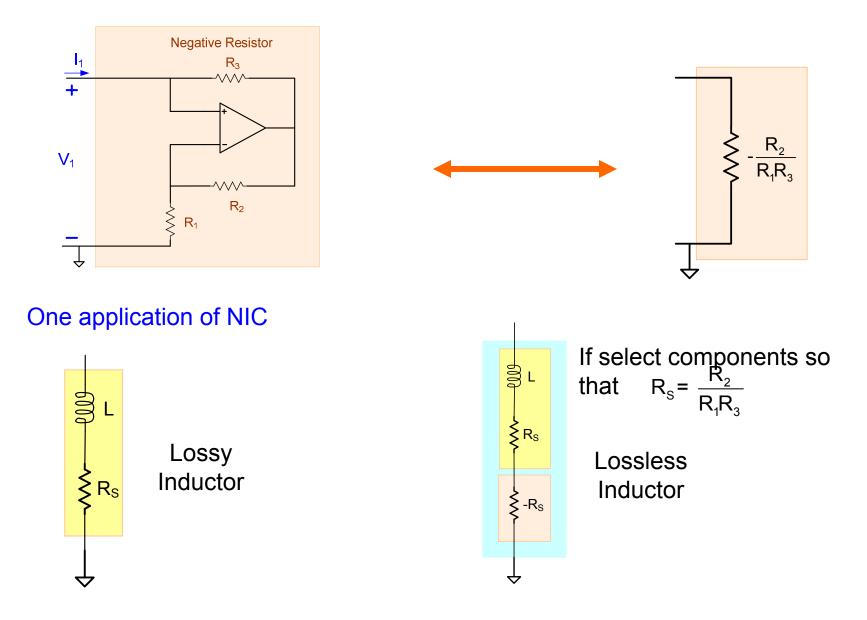


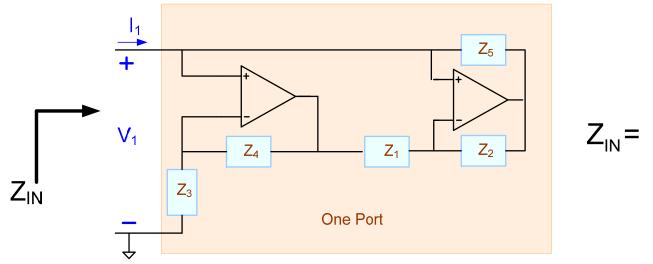
If $Z_2 = 1/sC$, $Z_1 = R_1$ and $Z_3 = R_3$, $Z_{IN} = -sCR_1R_3$

Modification of NIC to provide a positive inductance:

Replace Z_1 itself with a second NIC that has a negative input impedance

Negative Impedance Converter

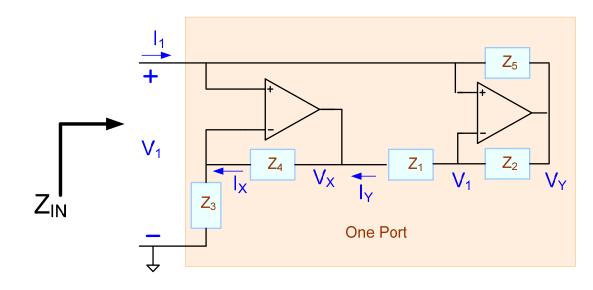




 $Z_{\rm IN} = \frac{Z_1 Z_3 Z_5}{Z_2 Z_4}$

This circuit is often called a Gyrator

Gyrator Analysis



$$I_{X} = V_{1}G_{3}$$

$$V_{X} = V_{1} + V_{1}G_{3} / G_{4} = V_{1}\left(1 + \frac{G_{3}}{G_{4}}\right)$$

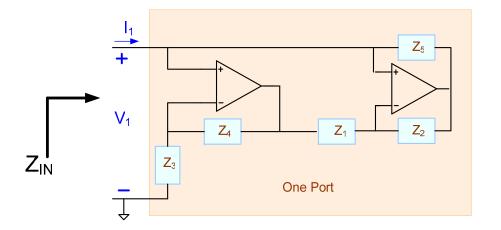
$$I_{Y} = (V_{1} - V_{X})G_{1} = V_{1}\left(-\frac{G_{3}}{G_{4}}\right)G_{1}$$

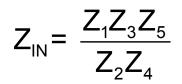
$$V_{Y} = V_{1} + I_{Y} / G_{2} = V_{1}\left(1 - \frac{G_{3}}{G_{4}}\left(\frac{G_{1}}{G_{2}}\right)\right)$$

$$I_1 = (V_1 - V_Y)G_5 = V_1 \left(\frac{G_3}{G_4} \left(\frac{G_1}{G_2}\right)\right)G_5$$

$$Z_{IN} = \frac{Z_1 Z_3 Z_5}{Z_2 Z_4}$$

Gyrator Applications





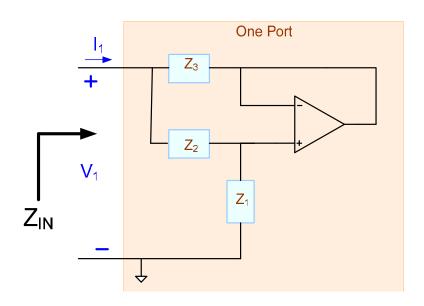
If
$$Z_1 = Z_3 = Z_4 = Z_5 = R$$
 and $Z_2 = 1/sC$ $Z_{IN} = (R^2C)s$ This is an inductor of value L=R²C

If
$$Z_2 = R_2$$
, $Z_3 = R_3$, $Z_4 = R_4$, $Z_5 = R_5$ and $Z_1 = 1/sC$ $Z_{IN} = \frac{R_3 R_5}{sCR_2 R_4}$

This is a capacitor of value $C_{EQ} = C \frac{R_2 R_4}{R_2 R_5}$

(can scale capacitance up or down)

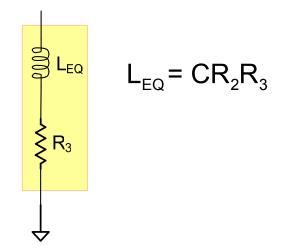
If $Z_2=Z_4=Z_5=R$ and $Z_1=Z_3=1/sC$ $Z_{IN}=(R^3C^2)s^2$ This is a "super" capacitor of value R^3C^2



$$\mathbf{I}_{1} = \left(\mathbf{V}_{1} - \left(\frac{\mathbf{Z}_{1}}{\mathbf{Z}_{1} + \mathbf{Z}_{2}}\right)\mathbf{V}_{1}\right)\mathbf{G}_{3}$$

$$Z_{IN} = Z_3 \left(1 + \frac{Z_2}{Z_1} \right)$$

If $Z_3 = R_3$, $Z_2 = R_2$ and $Z_1 = 1/sC$ $Z_{IN} = R_3 + s(CR_2R_3)$



End of Lecture 15